SADHANA EDUCATION SOCIETY'S
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L. S. RAHEJA COLLEGE OF ARTS & COMMERCE

Estd : 1980
NAAC ACCREDIDATED B++ (3rd Cycle)
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"AUTONOMOUS"

LSRC/EXAM/BSCIT/REGULAR/SEM I SH24 NEP/2024-25/3597

Date: 18.11.2024

REVISED TIME TABLE OF SEMESTER I REGULAR NOVEMBER 2024 (NEP)

PROGRAMME- BACHELOR OF SCIENCE (INFORMATION TECHNOLOGY)

DAY AND DATE	TIME	PAPER
TUESDAY, 26TH NOVEMBER 2024	11.00 AM TO 12.30 PM	INTRODUCTION TO PROGRAMMING WITH C
WEDNESDAY, 27TH NOVEMBER 2024	11.00 AM TO 12.30 PM	DIGITAL ELECTRONICS & LOGIC DESIGN
THURSDAY, 28TH NOVEMBER 2024	11.00 AM TO 12.00 PM	PRINCIPLES OF DISCRETE STRUCTURES AND ALGORITHMIC TECHNIQUES
FRIDAY, 29TH NOVEMBER 2024	11.00 AM TO 12.00 PM	DIGITAL MARKETING
SATURDAY, 30TH NOVEMBER 2024	11.00 AM TO 12.00 PM	WEB DESIGNING WITH HTML
MONDAY, 02ND DECEMBER 2024	11.00 AM TO 12.00 PM	COMMUNICATION SKILLS AND PRESENTATION TECHNIQUES
TUESDAY, 03RD DECEMBER 2024	11.00 AM TO 12.00 PM	GREEN IT
WEDNESDAY, 04TH DECEMBER 2024	11.00 AM TO 12.00 PM	INTRODUCTION TO INDIAN KNOWLEDGE SYSTEM
THURSDAY, 05TH DECEMBER 2024	9.00 AM TO 11.00 AM	INTRODUCTION TO PROGRAMMING WITH C (LAB)
THURSDAY, 05TH DECEMBER 2024	12.00 PM TO 2.00 PM	DIGITAL ELECTRONICS & LOGIC DESIGN (LAB)

IMPORTANT NOTES:

1. The candidates appearing for the examination should report 30 minutes prior to commencement of examination.
2. ID card and Hall Ticket is compulsory.

(DR. PRAJAKTA JOSHI)
BSCIT COORDINATOR

(DR. SEEMA UKIDVE)
CONVENOR
BOARD OF EXAMINATION AND EVALUATION

(DR. DEBAJIT SARKAR)
PRINCIPAL