



ISO Certified 9001-2015

SADHANA EDUCATION SOCIETY'S
(Registered under society's act of 1860 & Bombay Public Trust Act 1950)
Linguistic (Gujarati) Minority

L. S. RAHEJA COLLEGE OF ARTS & COMMERCE

Estd : 1980

NAAC ACCREDITED B++ (3rd Cycle)

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"AUTONOMOUS"

LSRC/EXAM/BSCIT/REGULAR/SEM I SH24 NEP/2024-25/3477

Date: 28.10.2024

TIME TABLE OF SEMESTER I REGULAR NOVEMBER 2024 (NEP)


PROGRAMME- BACHELOR OF SCIENCE (INFORMATION TECHNOLOGY)

DAY AND DATE	TIME	PAPER
TUESDAY, 26 TH NOVEMBER 2024	11.00 AM TO 01.00 PM	INTRODUCTION TO PROGRAMMING WITH C
WEDNESDAY, 27 TH NOVEMBER 2024	11.00 AM TO 01.00 PM	DIGITAL ELECTRONICS & LOGIC DESIGN
THURSDAY, 28 TH NOVEMBER 2024	11.00 AM TO 12.00 PM	PRINCIPLES OF DISCRETE STRUCTURES AND ALGORITHMIC TECHNIQUES
FRIDAY, 29 TH NOVEMBER 2024	11.00 AM TO 12.00 PM	DIGITAL MARKETING
SATURDAY, 30 TH NOVEMBER 2024	11.00 AM TO 12.00 PM	WEB DESIGNING WITH HTML
MONDAY, 02 ND DECEMBER 2024	11.00 AM TO 12.00 PM	COMMUNICATION SKILLS AND PRESENTATION TECHNIQUES
TUESDAY, 03 RD DECEMBER 2024	11.00 AM TO 12.00 PM	GREEN IT
WEDNESDAY, 04 TH DECEMBER 2024	11.00 AM TO 12.00 PM	INTRODUCTION TO INDIAN KNOWLEDGE SYSTEM
THURSDAY, 05 TH DECEMBER 2024	9.00 AM TO 11.00 AM	INTRODUCTION TO PROGRAMMING WITH C (LAB)
THURSDAY, 05 TH DECEMBER 2024	12.00 PM TO 2.00 PM	DIGITAL ELECTRONICS & LOGIC DESIGN (LAB)

IMPORTANT NOTES:

1. The candidates appearing for the examination should report 30 minutes prior to commencement of examination.
2. ID card and Hall Ticket is compulsory.


(MS. PRAJAKTA JOSHI)
BSCIT COORDINATOR


(DR. SEEMA UKIDVE)
CONVENOR
BOARD OF EXAMINATION AND EVALUATION


(DR. DEBAJIT SARKAR)
PRINCIPAL

